

00000000000000000000000000000000

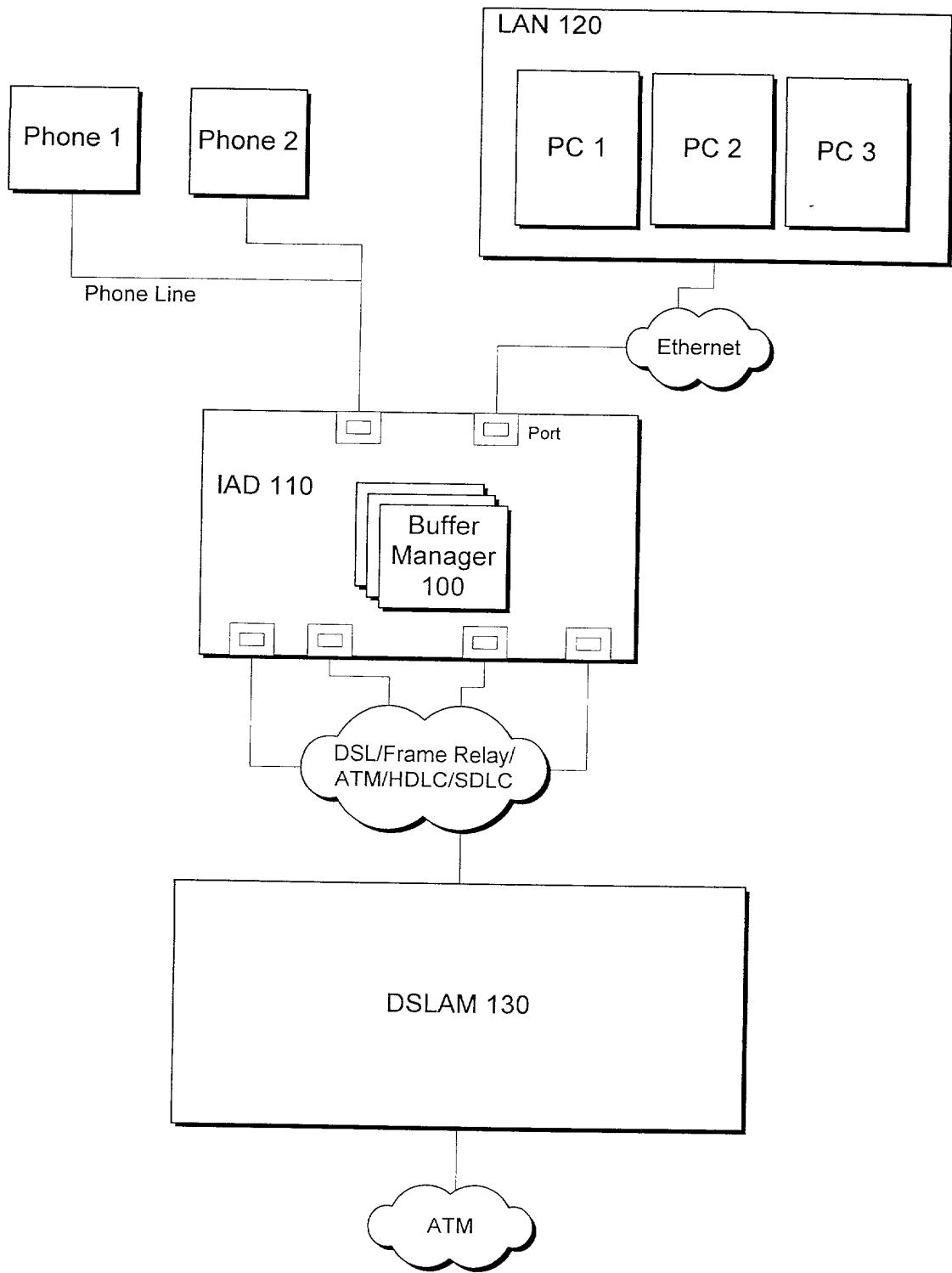


FIG. 1

IAD 110

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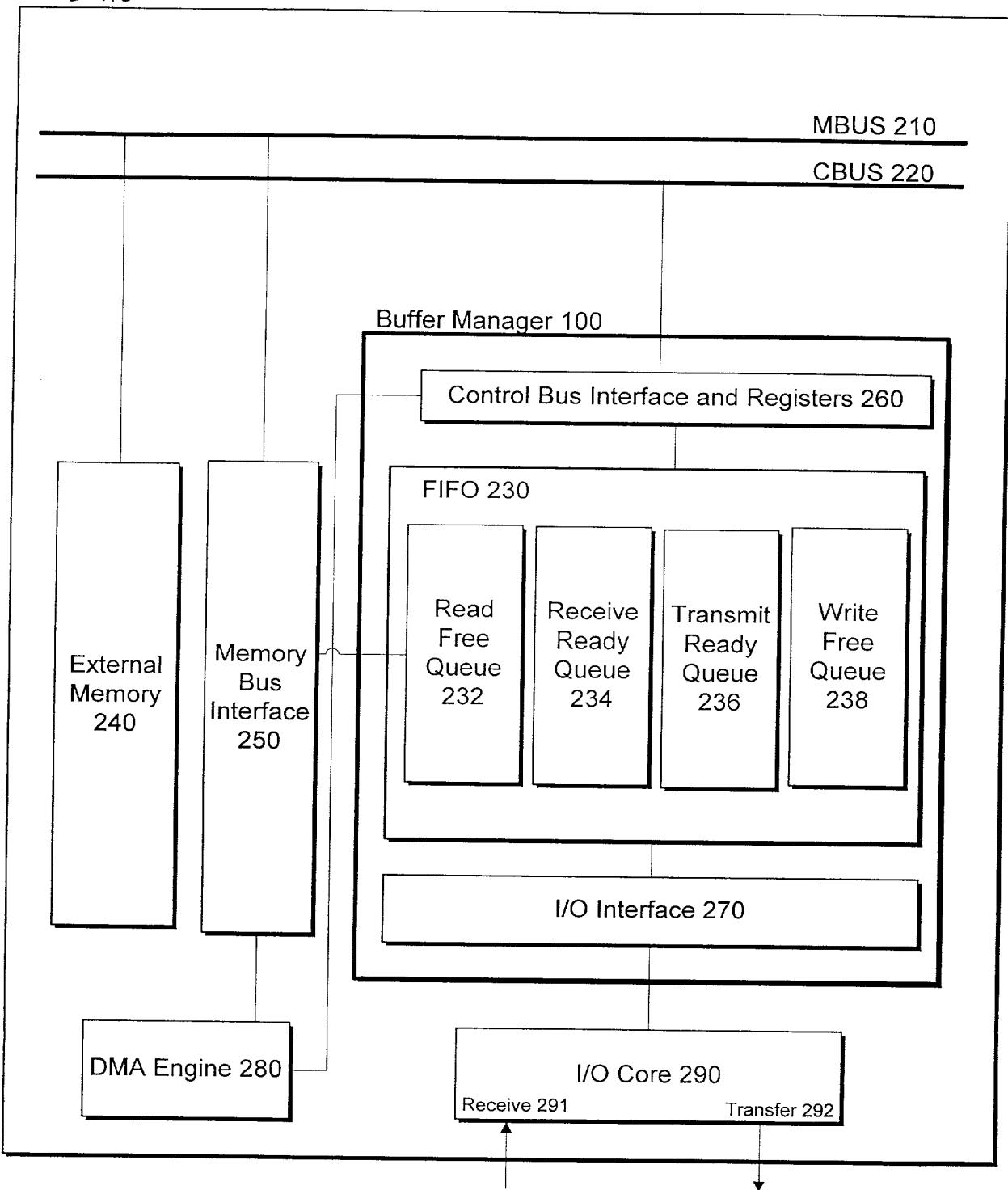


FIG. 2

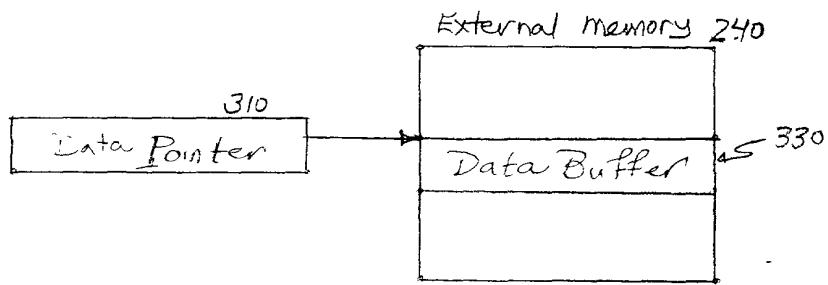


FIG. 3A

Buffer Descriptor (BD) 300

300

32-bits
Data
Pointer
310

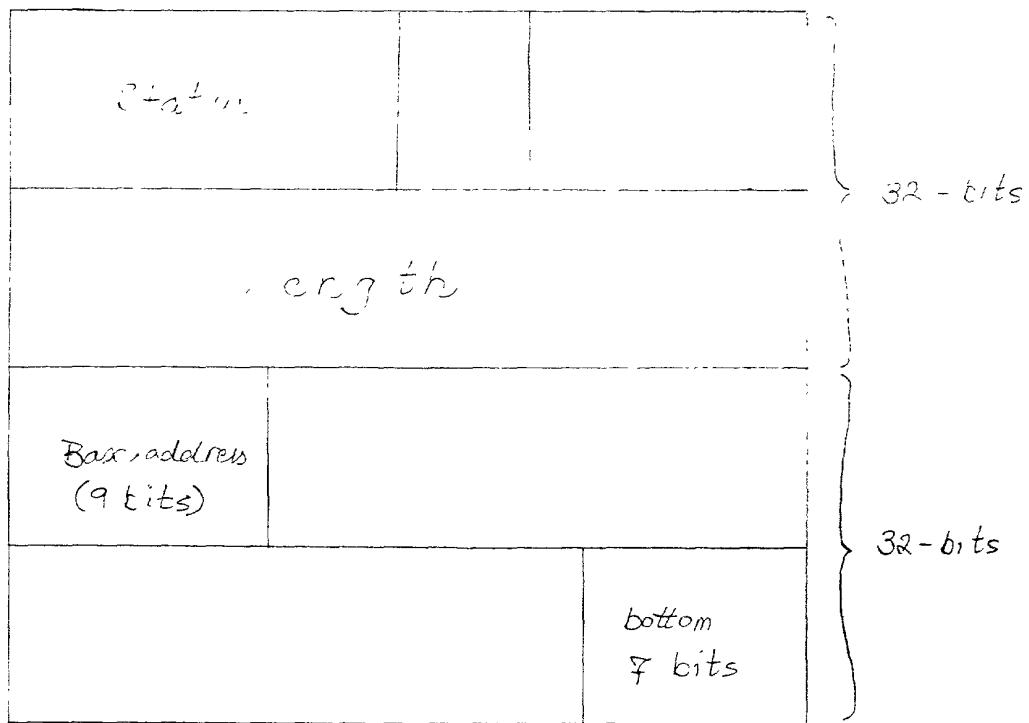


FIG. 3B

Configuration	Bit 8	Bit 7	Transmit Queue #	Depth
1	0	0	1-4	32
2	0	1	1-2	40
			3-4	24
3	1	0	1	80
			2-3	24
			4	0
4	1	1	1	64
			2	64
			3	0
			4	0

Transmit Ready Queue Configuration

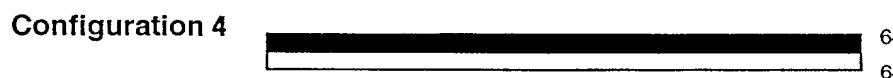
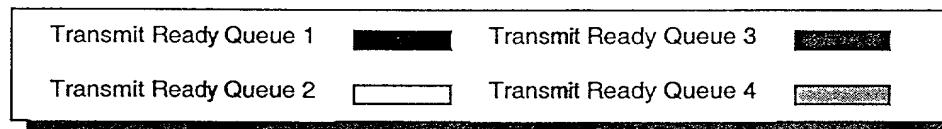


FIG. 4

00000000000000000000000000000000

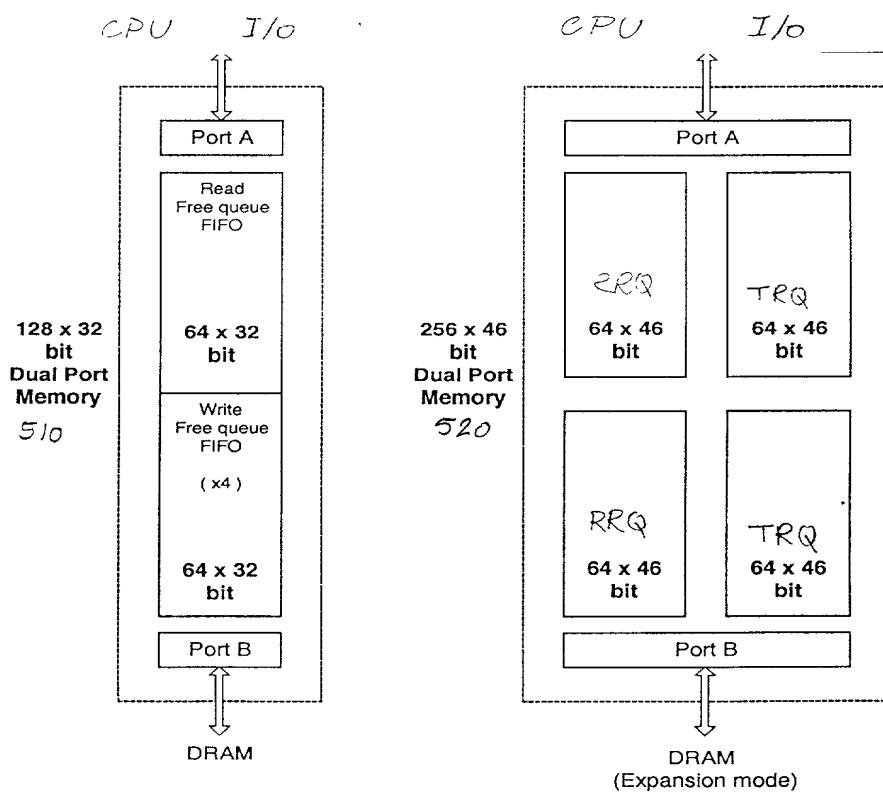


FIG. 5

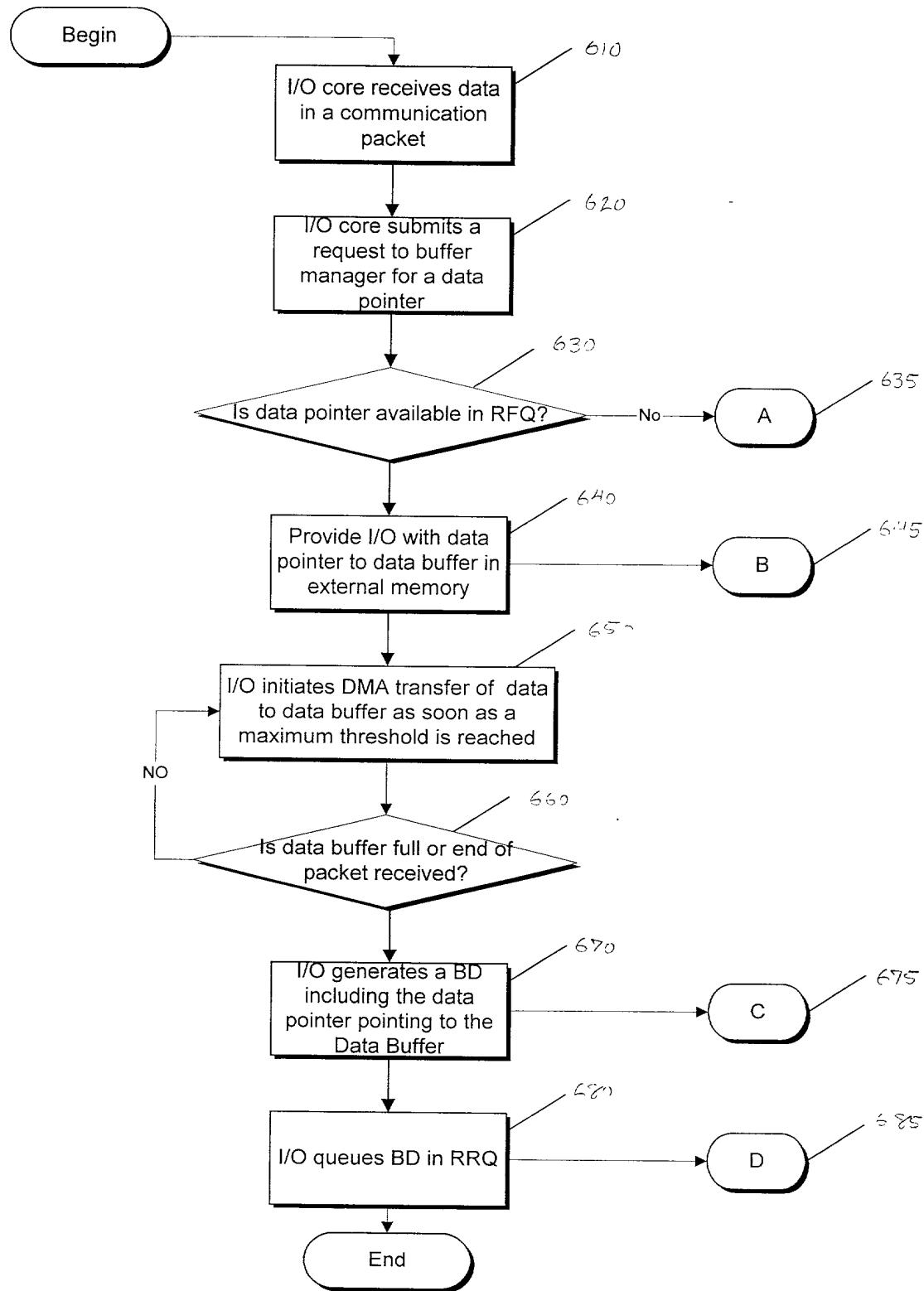


FIG.6

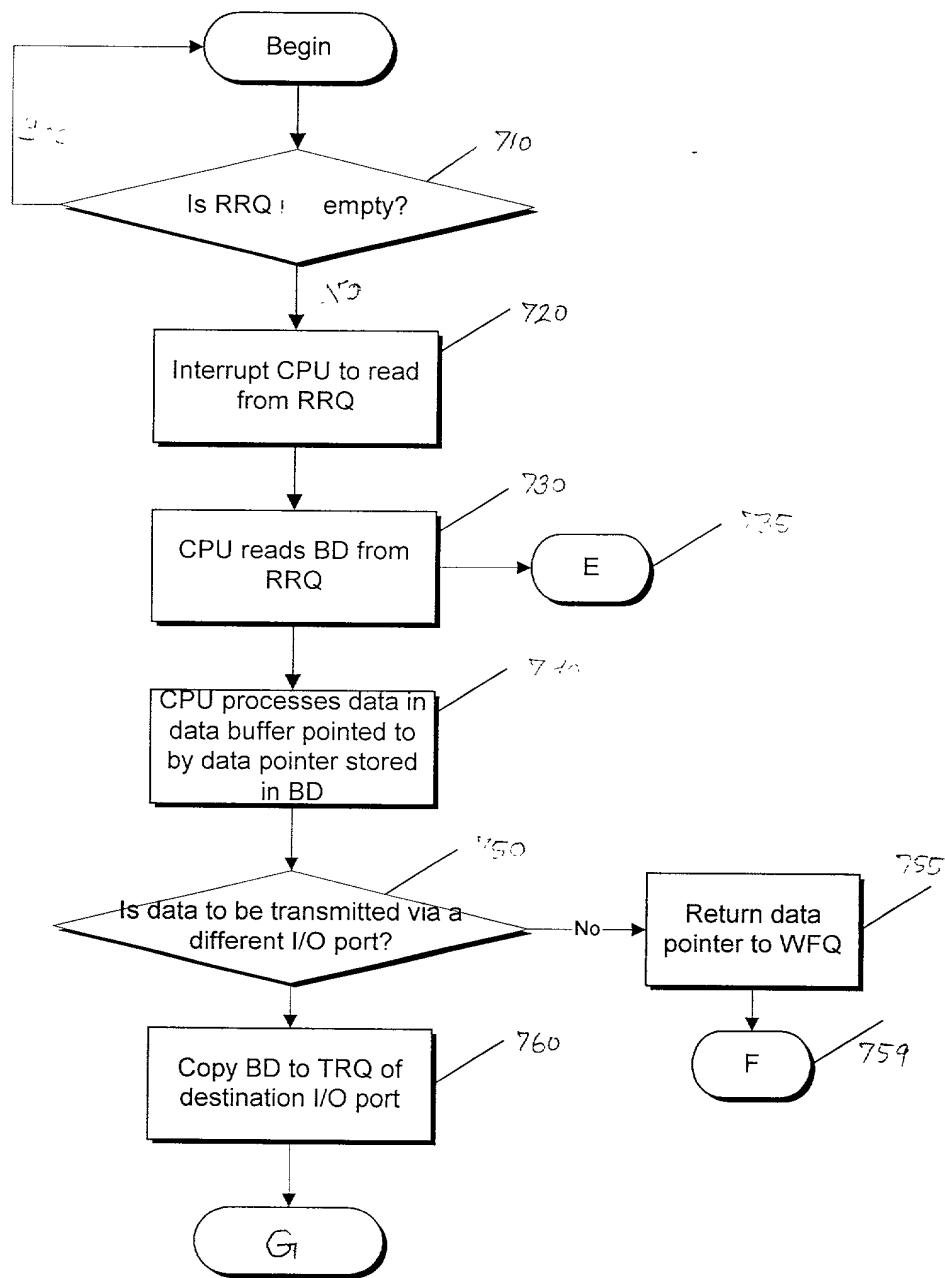


FIG. 7

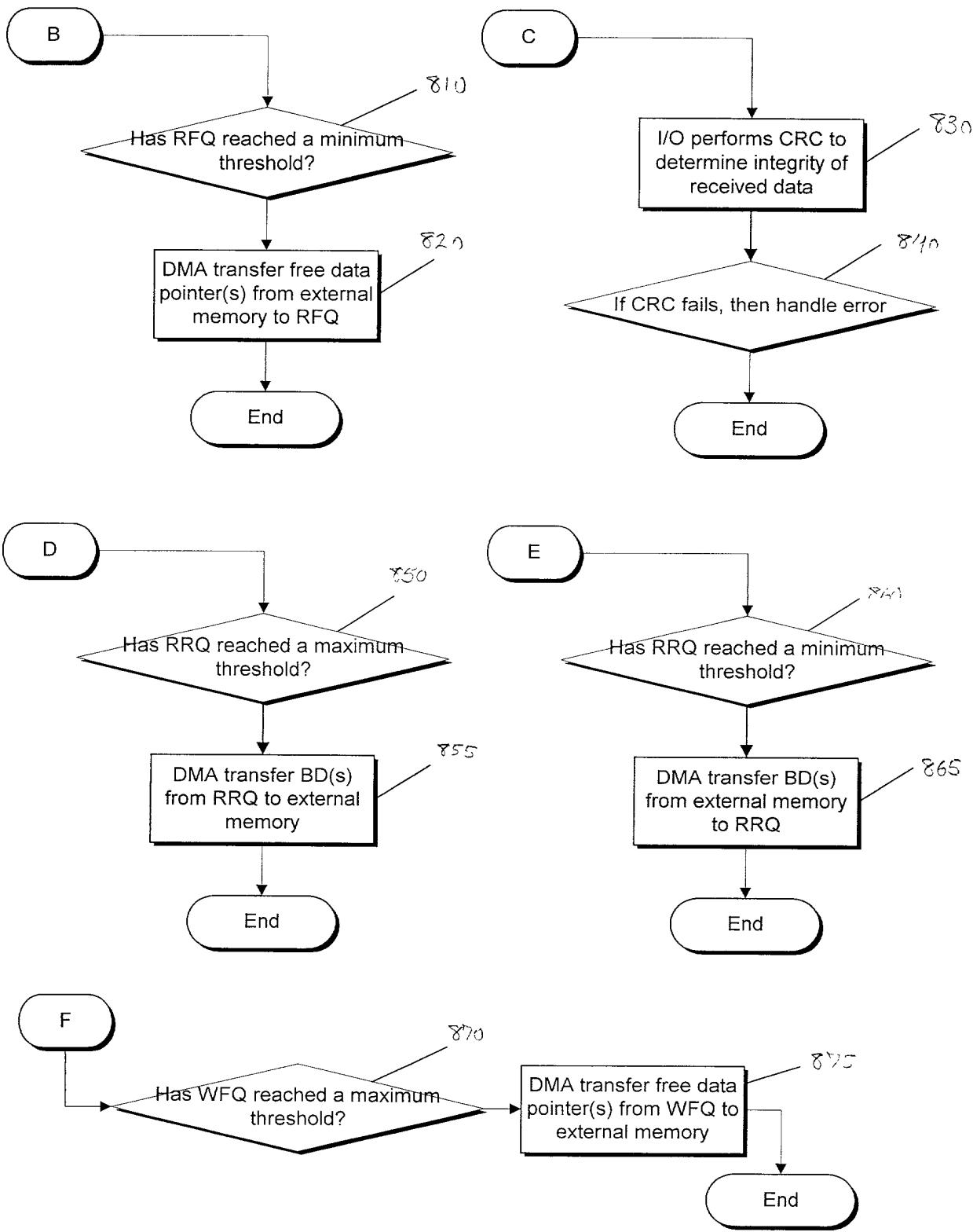


FIG. 8

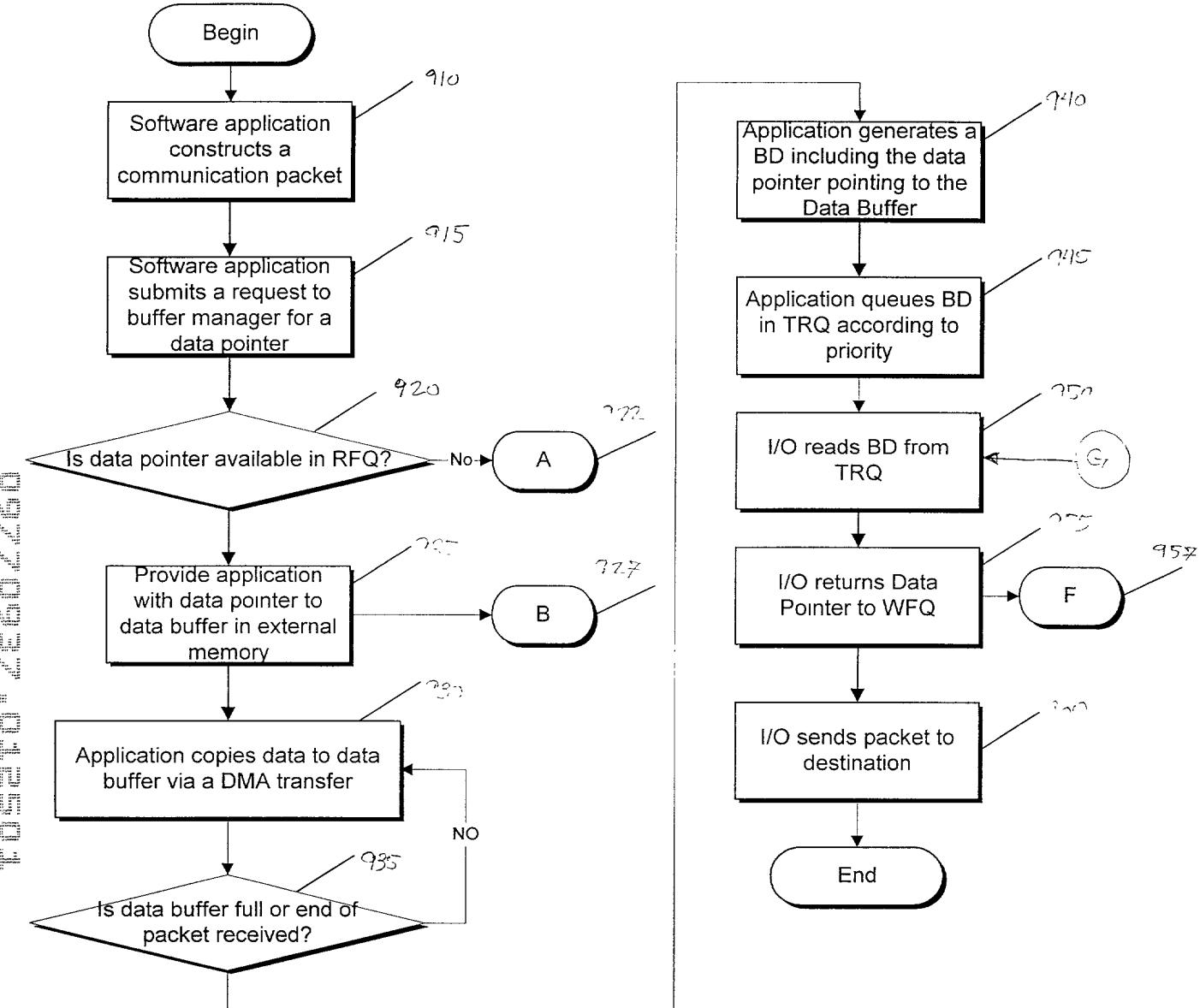


FIG. 9

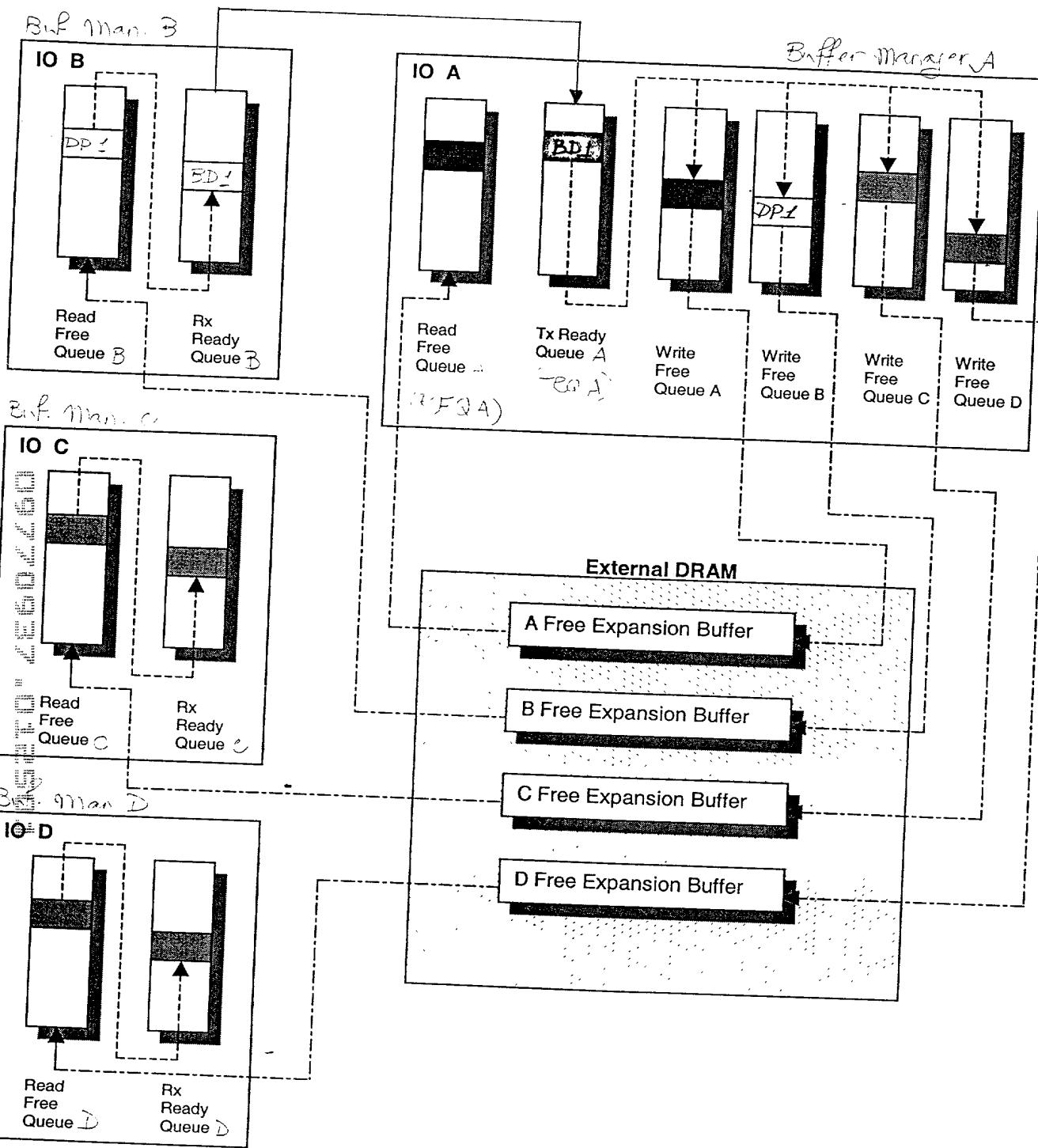


FIG. 10

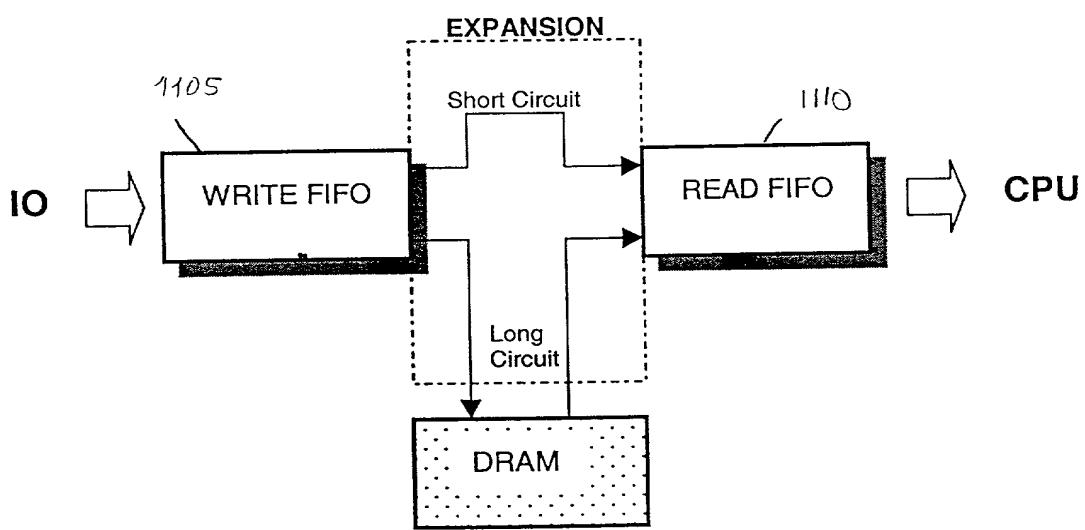


FIG. 11

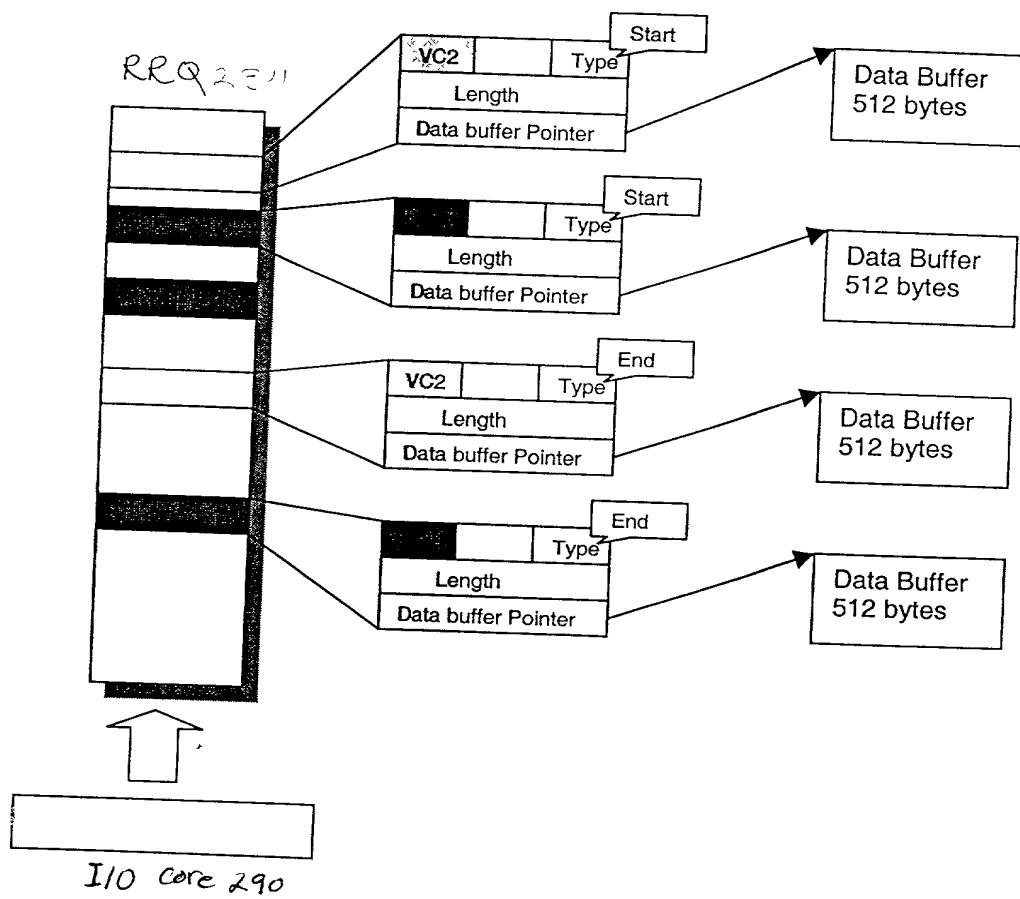


FIG. 12

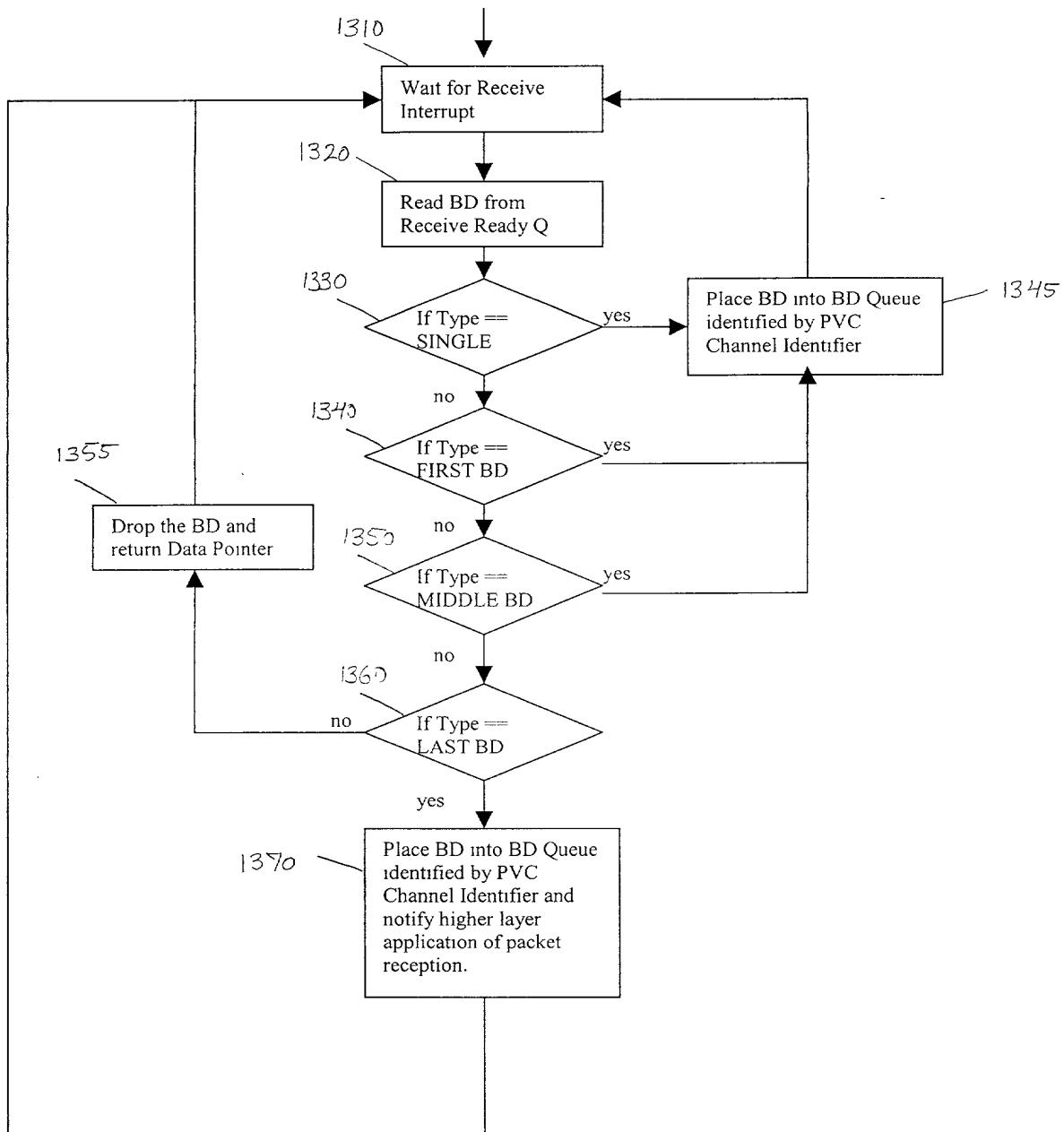


FIG. 13

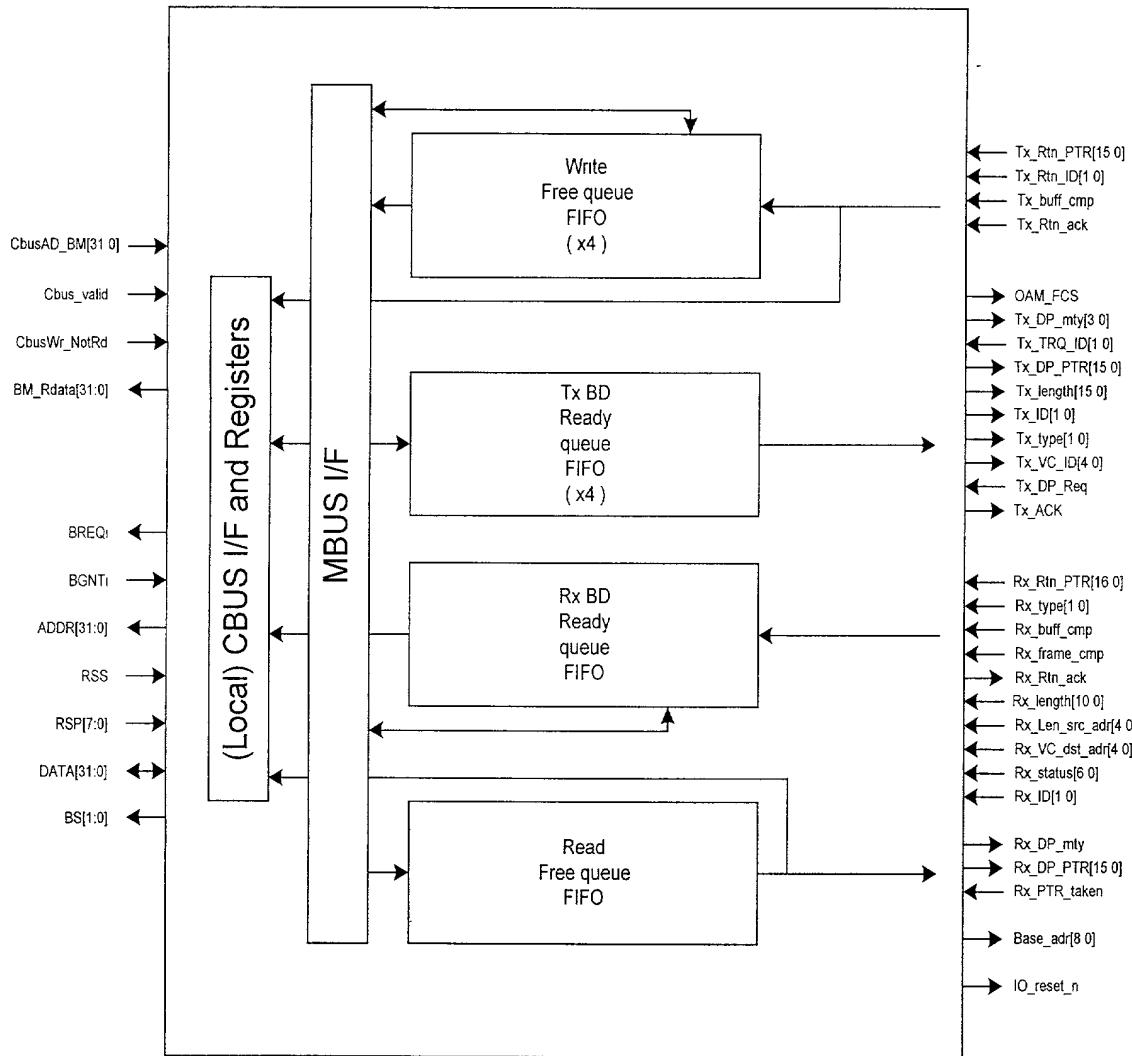


FIG. 14